

## IP Evaluation Meeting

<b>Activity Name</b>	IP Evaluation Meeting		
<b>Mode of Conduct</b>	Offline		
<b>Time</b>	2:30 pm to 3:30 pm - 28-06-2024		
<b>Participants</b>	<b>S.NO</b>	<b>NAME</b>	<b>DESIGNATION</b>
	1.	Dr. S. Baskar (Chairperson)	Dean R&D, TCE, Madurai.
	2.	Mr. Kaja Bandha Navas	Assistant Professor, Department of Fashion Technology, National Institute of Fashion Technology, Ministry of Textiles, Gujarat.
	3.	Ms. Suganya Jayavelu	Patent Analyst, PUTHRAN & ASSOCIATES, Chennai.
	4.	Mr. P. Sebin Sunny	CEO, Technology Business Incubator, TCE, Madurai.
	5.	Dr. S. Charles Raja	Associate Dean, Sponsored Research, TCE.
	6.	Dr. B. Sathya Bama	CLC, IPR Cell, TCE
	7.	Other inventors of TCE	
<b>Description</b>	The event was an IP Evaluation Meeting conducted by the TCE IPR CELL (R&D) at Thiagarajar College of Engineering, Madurai on 28-06-2024 from 2:30 PM to 3:30 PM. The purpose of the meeting was to scrutinize the patent applications submitted by TCE inventors (IDF_2024_7_01 to IDF_2024_7_05). The inventors presented their proposals, and the committee evaluated the patentability and commercial viability of the submitted IDFs.		



THIAGARAJAR COLLEGE OF ENGINEERING, Madurai – 625 015  
(A Govt. Aided Autonomous Institution Affiliated to Anna University)

TCE IPR CELL (R&D)  
Minutes of the IP Evaluation Meeting

**Agenda of the Meeting:** To scrutinize the patent applications submitted by TCE inventors (Nos. IDF\_2024\_7\_01 to IDF\_2024\_7\_05) by IPEC members.

**Date:** 28-06-2024

**Time:** 2.30 AM to 3.30 PM

**Members Attended:**

S.No	Name	Designation
1	Dr.S.Baskar, (Chairperson)	Dean R&D, TCE, Madurai.
2	Mr.Kaja Bandha Navas,	Assistant Professor, Department of Fashion Technology, National Institute of Fashion Technology, Ministry of Textiles, Gujarat.
3	Ms.Suganya Jayavelu,	Patent Analyst, PUTHRAN & ASSOCIATES, Chennai.
4	Mr.P.Sebin Sunny,	CEO, Technology Business Incubator, TCE, Madurai.
5	Dr. S. Charles Raja	Associate Dean, Sponsored Research, TCE.
6	Dr.B.Sathya Bama	CLC, IPR Cell, TCE
7	Inventors of TCE	

As per TCE IP Policy, a scrutiny meeting was conducted to examine the patentability and commercial viability of IDFs submitted by faculty and student members of TCE. The inventors of Five IDFs (Nos. IDF\_2024\_7\_01 to IDF\_2024\_7\_05) have made a presentation to the committee. The committee recommendations and Comments are given below.

Sl. No.	TCE IDF No.	IDF Title	Inventors Name	Comments
1.	IDF_2024_7_01_ECE	Double Gate Fin Field Effect Transistor for the Thyroid Cancer Cells Detection	Dr.N.B.Bala Murugan	Category I
2.	IDF_2024_7_02_EEE	Advanced Volt Ampere and Time-based Accurate fault Restricting device	Dr.R.Rajan Prakash	Category II
3.	IDF_2024_7_03_EEE	Image Processing based Controller for Indoor Cooling Unit	Dr.PS.Manoharan	Need Revision
4.	IDF_2024_7_04_ECE	Smart linear LPG gas regulation system for cooking based on Temperature sensor	Dr.D. Gracia Nirmala Rani	Need Revision
5.	IDF_2024_7_05_EEE	Online Voting System	Dr.PS.Manoharan	Need Revision

Mr.Kaja Bandha Navas	Ms.Suganya Jayavelu	Dr.S.Baskar
Evaluator 1	Evaluator 2	Dean R&D



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TCE IPR CELL

Ref: TCE/IPR Cell/03/24/07/01

16-07-2024

Submitted to The Chairman & Correspondent, TCE, Madurai

Sir,

**Sub: Requisition of Approval for Patent filing - Regd**

As part of the patent filing process, the following patent applications have been selected for filing in category 1 (Patentable & Commercially Viable) and Category 2 (Only Patentable-Self filing with drafting support) in the set 6 and set 7 evaluations of ten applications.

*Approved  
16/7/24*

S.No	Patent Detail	Faculty Name	Department	Filing Charges (upto Grant)	Consultant
1.	Double Gate Fin Field Effect Transistor for the Thyroid Cancer Cells Detection	Dr.N.B.Bala Murugan	ECE	1,24,000/-	M/S Altacit Global, Chennai. (Category I)
2.	Semi-automatic Electronic Chip Dispenser	Dr. K.Rajeshwari	ECE	44,000/-	Self-filing with Drafting support from external agency. (Category II)
3.	Automatic Cleaning Robots for Dining Tables	Dr. S.Karthikeyan	MECH	44,000/-	
4.	C-shaped Single Nanosheet (NS) with Dual Channel FETs	Dr. N.B.BalaMurugan	ECE	44,000/-	
5.	Advanced Volt Ampere and Time-based Accurate fault Restricting device	Dr.R.Rajan Prakash	EEE	44,000/-	

To proceed for the next stages up to grant, we request your kind approval with the approved charges and the expenses may be met from .....S/F.....A/c.

Thanking You

Yours faithfully,

1. Chairman's Approval for patent fee
2. Chairman's approval for Category 1 & 2
3. Minutes of the Scrutiny Meeting

*AS* *M. Palanisubramanian* *16/7/24*