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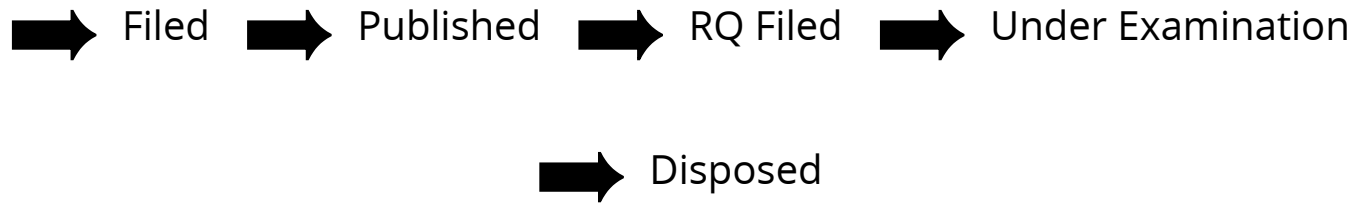
Application Details

APPLICATION NUMBER	202141041241
APPLICATION TYPE	ORDINARY APPLICATION
DATE OF FILING	14/09/2021
APPLICANT NAME	THIAGARAJAR COLLEGE OF ENGINEERING
TITLE OF INVENTION	AN EFFICIENT HIGH SPEED VLSI ARCHITECTURE IN PARALLEL FOR PERFORMING SCALAR MULTIPLICATION IN BINARY
FIELD OF INVENTION	COMPUTER SCIENCE
E-MAIL (As Per Record)	
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E-MAIL (UPDATED Online)	
PRIORITY DATE	
REQUEST FOR EXAMINATION DATE	10/01/2022
PUBLICATION DATE (U/S 11A)	31/12/2021

Application Status

APPLICATION STATUS	Abandoned U/s 21(1)
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