

Office of the Controller General of Patents, Designs & Trade Marks Department of Industrial Policy & Promotion, Ministry of Commerce & Industry, Government of India

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Application Details	
APPLICATION NUMBER	201841040224
APPLICATION TYPE	ORDINARY APPLICATION
DATE OF FILING	24/10/2018
APPLICANT NAME	THIAGARAJAR COLLEGE OF ENGINEERING
TITLE OF INVENTION	A NOVEL MODULATION TECHNIQUE FOR GRID CONNECTED THREE PHASE QUASI Z SOURCE LINE SYNCHRONIZED INVERTER
FIELD OF INVENTION	ELECTRICAL
E-MAIL (As Per Record)	ipr@puthrans.com
ADDITIONAL-EMAIL (As Per Record)	ipr@puthrans.com
E-MAIL (UPDATED Online)	
PRIORITY DATE	
REQUEST FOR EXAMINATION DATE	07/09/2019
PUBLICATION DATE (U/S 11A)	01/05/2020
REPLY TO FER DATE	04/10/2021

Application Status	
APPLICATION STATUS	Reply Filed. Application in amended examination

View Documents



In case of any discrepancy in status, kindly contact ipo-helpdesk@nic.in